# Schottky Rectifier, 3.5 A

#### **FEATURES**

- Popular D-PAK outline
- Small foot print, surface mountable
- · Low forward voltage drop
- High frequency operation
- · Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

#### DESCRIPTION

The VS-30WQ04FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	3.5	А		
V <sub>RRM</sub>		40	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	500	А		
V <sub>F</sub>	3 Apk, T <sub>J</sub> = 125 °C	0.49	V		
TJ		- 40 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-30WQ04FNPbF	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	40	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	40	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at $T_C$ = 135 °C, rectangular waveform		3.5	
Maximum peak one cycle non-repetitive surge current		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	500	А
See fig. 7	IFSM	10 ms sine or 6 ms rect. pulse	V <sub>RRM</sub> applied	80	
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 16 mH		8.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1.0	А



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PRODUCT SUMMARY			
Package	D-PAK (TO-252AA)		
I <sub>F(AV)</sub>	3.5 A		
V <sub>R</sub>	40 V		
$V_F$ at $I_F$	See Electrical table		
I <sub>RM</sub>	24 mA at 125 °C		
T <sub>J</sub> max.	150 °C		
Diode variation	Single die		
E <sub>AS</sub>	8 mJ		





## VS-30WQ04FNPbF

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# VS-30WQ04FNPbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	3 A	T <sub>J</sub> = 25 °C	0.53	v
		6 A		0.67	
		3 A	T <sub>J</sub> = 125 °C	0.49	
		6 A		0.62	
Maximum reverse leakage current See fig. 2	I <sub>RM</sub> <sup>(1)</sup>	$T_J = 25 \ ^\circ C$	V <sub>R</sub> = Rated V <sub>R</sub>	2	mA
		T <sub>J</sub> = 125 °C		24	
Threshold voltage	V <sub>F(TO)</sub>	- T <sub>J</sub> = T <sub>J</sub> maximum		0.34	V
Forward slope resistance	r <sub>t</sub>			37.33	mΩ
Typical junction capacitance	CT	$V_{R}$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		189	pF
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>		10 000	V/µs

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		- 40 to 150	°C
Maximum thermal resistance, junction to case	R <sub>thJC</sub>	DC operation See fig. 4	4.7	°C/W
Approximate weight			0.3	g
			0.01	oz.
Marking device		Case style D-PAK (similar to TO-252AA)	30WQ04FN	

#### Note

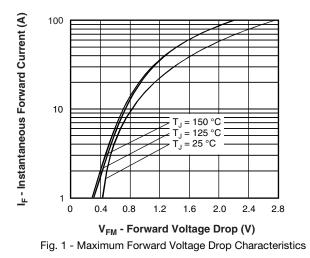
<sup>(1)</sup>  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink

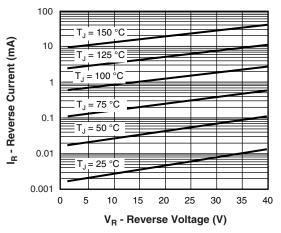


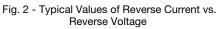
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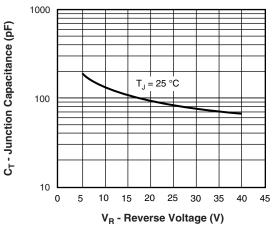


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

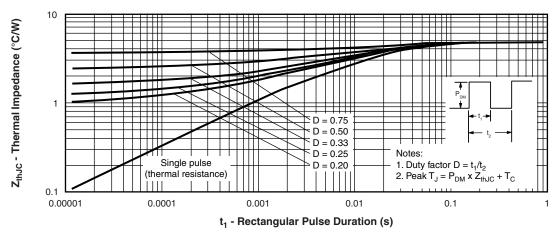


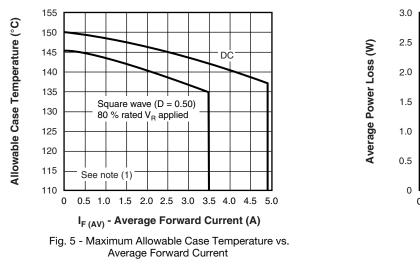
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

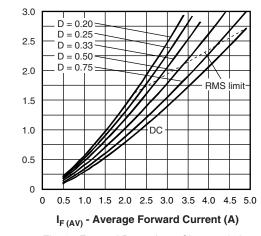
# VS-30WQ04FNPbF

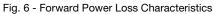
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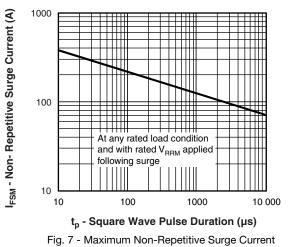
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#### Note

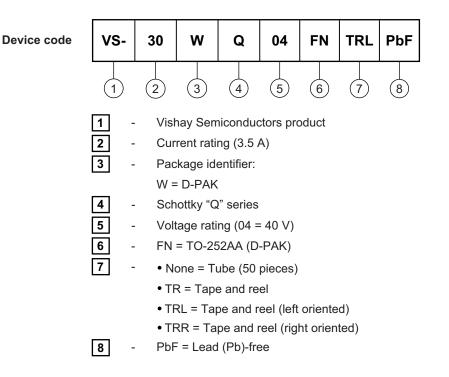
- <sup>(1)</sup> Formula used:  $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$ ;
- $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \ x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \ x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$



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#### ORDERING INFORMATION TABLE

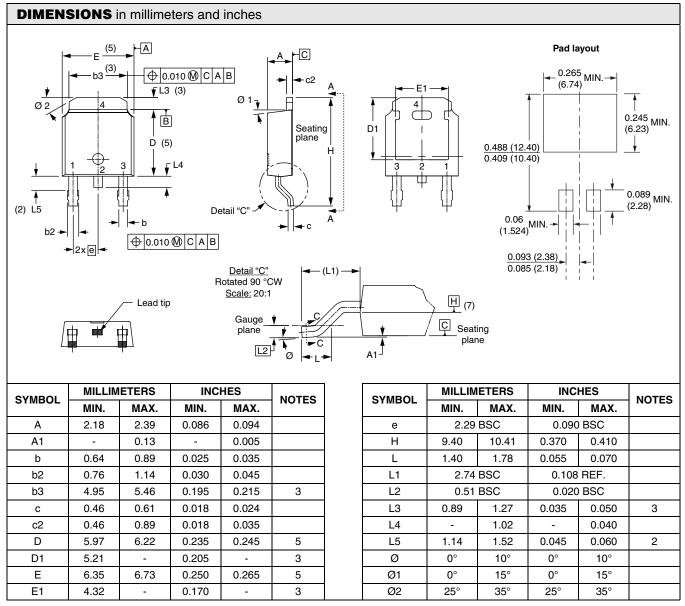


LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95016				
Part marking information	www.vishay.com/doc?95059			
Packaging information	www.vishay.com/doc?95033			
SPICE model	www.vishay.com/doc?95288			



Vishay High Power Products

## D-PAK (TO-252AA)



#### Notes

- $^{(1)}\,$  Dimensioning and tolerancing as per ASME Y14.5M-1994
- <sup>(2)</sup> Lead dimension uncontrolled in L5
- <sup>(3)</sup> Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad
- (4) Section C C dimension apply to the flat section of the lead between 0.13 and 0.25 mm (0.005 and 0.10") from the lead tip
- <sup>(5)</sup> Dimension D, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- <sup>(6)</sup> Dimension b1 and c1 applied to base metal only
- <sup>(7)</sup> Datum A and B to be determined at datum plane H
- <sup>(8)</sup> Outline conforms to JEDEC outline TO-252AA



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